L Number	Hits	Search Text	DB	Time stamp
31	1	6003038.pn.	USPAT; EPO	2004/02/23 07:23
-	1	5479621.pn.	USPAT; EPO	2004/02/20 13:50
-	1	EP-279953-$.pn.	USPAT; EPO	2004/02/11 13:56
-	3	emulation adj instruction	IBM_TDB	2004/02/11 14:01
-	1	"real-time cisc architecture hw emulator"	IBM_TDB	2004/02/11 14:02
-	1	5752073.pn.	USPAT; EPO	2004/02/11 14:03
-	13	(("8-bit" or ("8" adj bit)) and (memory or processor or microprocessor)).ti.	USPAT; EPO	2004/02/12 08:17
-	1514	(("8-bit" or ("8" adj bit)) adj (processor or microprocessor))	USPAT; EPO	2004/02/12 08:16
-	77	712/242.ccls.	USPAT; EPO	2004/02/12 08:17
-	71	712/243.ccls.	USPAT; EPO	2004/02/12 08:17
-	21	(712/242.ccls. or 712/243.ccls.) and ("8-bit" or (((("8-bit" or ("8" adj bit)) near2 (processor or microprocessor))) adj bit))	USPAT; EPO	2004/02/12 08:18
-	11	(712/242.ccls. or 712/243.ccls.) and (("8-bit" or (((("8-bit" or ("8" adj bit)) near2 (processor or microprocessor))) adj bit)) with instruction)	USPAT; EPO	2004/02/12 09:24
-	0	(712/242.ccls. or 712/243.ccls.) and bytecode	USPAT; EPO	2004/02/12 09:24
-	719	bytecode	USPAT; EPO	2004/02/12 09:24
-	49	bytecode and 712.clas.	USPAT; EPO	2004/02/12 09:27
-	153	bytecode with $4processor	USPAT; EPO	2004/02/12 10:41
-	67	(bytecode with $4processor) and $3routine	USPAT; EPO	2004/02/12 10:43
-	239	(((user adj defined) or predefined) adj instruction)	USPAT; EPO	2004/02/12 10:44
-	202	(((user adj defined) or predefined) adj instruction) and (processor or microprocessor)	USPAT; EPO	2004/02/12 10:44
-	80	(((user adj defined) or predefined) adj instruction) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit")	USPAT; EPO	2004/02/12 10:45
-	31	(((user adj defined) or predefined) adj instruction) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit") and subroutine	USPAT; EPO	2004/02/12 10:47
-	1	((((user adj defined) or ("user-defined")) with predefined) with instruction) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit") and subroutine	USPAT; EPO	2004/02/12 10:49
-	68	(((user adj defined) or ("user-defined")) with instruction) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit") and subroutine	USPAT; EPO	2004/02/12 10:49
-	1	(((user adj defined) or ("user-defined")) with instruction with subroutine) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit")	USPAT; EPO	2004/02/12 10:50
-	11	(macroinstruction with subroutine) and (processor or microprocessor) and (bytecode or "8-bit" or "8 bit")	USPAT; EPO	2004/02/12 10:50
-	1	6003038.pn.	USPAT; EPO	2004/02/17 10:59
-	14	("5446902" | "5511196" | "5615362" | "5632034" | "5682532" | "5692193" | "5706506" | "5736984" | "5740441" | "5745758" | "5787431" | "5797007" | "5815718" | "5848423").PN.	USPAT	2004/02/17 10:57

-	208	712/209.ccls.	USPAT; EPO	2004/02/17 11:02
-	106	717/118.ccls.	USPAT; EPO	2004/02/17 11:03
-	88	717/118.ccls. and processor	USPAT; EPO	2004/02/17 11:03
-	83	717/118.ccls. and processor and memory	USPAT; EPO	2004/02/17 11:04
-	51	717/118.ccls. and processor and memory and address	USPAT; EPO	2004/02/17 11:04
-	13	717/118.ccls. and processor and memory and address and fetch$3	USPAT; EPO	2004/02/17 11:16
-	146	bytecode adj execut$3	USPAT; EPO	2004/02/17 11:17
-	0	(bytecode adj execut$3) with (microcode or microinstruction)	USPAT; EPO	2004/02/17 11:18
-	0	(bytecode adj execut$3) with (subroutine)	USPAT; EPO	2004/02/17 11:18
-	1	6349377.pn.	USPAT; EPO	2004/02/19 16:37
-	72	subroutine near4 (data adj memory)	USPAT; EPO	2004/02/19 17:12
-	210	routine near4 (data adj memory)	USPAT; EPO	2004/02/19 17:12
-	42	routine near4 store$ near4 (data adj memory)	USPAT; EPO	2004/02/19 17:12
-	2276	shift$3 near address	USPAT; EPO	2004/02/20 13:53
-	298	(shift$3 near address) with generat$3	USPAT; EPO	2004/02/20 13:59
-	107	(shift$3 near address) near3 generat$3	USPAT; EPO	2004/02/20 14:00
-	12	(shift$3 with address) and bytecode	USPAT; EPO	2004/02/20 14:10
-	1	(shift$3 with address) same bytecode same ("program counter" or "instruction register")	USPAT; EPO	2004/02/20 14:11
-	46	("8-bit" or "8 bit") with ("instruction memory")	USPAT; EPO	2004/02/22 11:33

C:\APPS\EAST\Workspaces\Java bytecode processor.wsp